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Verified and Acknowledged Examiner's Signature <i>[Signature]</i> <i>mtt</i> Initials				
ADDRESS Jack V. Musgrove SKJERVEN MORRILL MacPHERSON LLP Suite 700 25 Metro Drive San Jose, CA 95110-1349				
TITLE Linear associative memory-based hardware architecture for fault tolerant ASIC/FPGA work-around				
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